

Amendments to the Specification:

Please replace the first full paragraph on page 9 with the following amended paragraph:

At a step 410, the computer system 100 determines whether a machine state memory card 200 is connected to the PCI-CardBus bridge 24. If detected, the transfer application 240 is invoked. The transfer application 240 identifies the machine state information for restoring a machine state at a step 414, and instructs the controller 212 to retrieve the appropriate machine state information from the memory 214 and transfer the data through the bus interface 210 to the computer system 100 at a step 418. The controller 212 negotiates with the PCI bridge 16 to obtain control of the PCI bus 18, and subsequently coordinates the transfer of the machine state information with the CPU 10 and/or the host-PCI bridge 16 to write the appropriate information in order to restore the machine state at a step 422. For example, the data present in the memory 12 at the time the selected machine state was stored in the machine state memory card 200 is rewritten to the memory 12 upon the restoration operation. Similarly, the data present in the various data registers of the CPU 10, and more generally, throughout the computer system 100, are rewritten to restore the machine state. When download of the machine state information is completed, the controller 212 relinquishes control of the PCI bus and normal computer system operation is resumed. If there is no memory card detected, the machine process<sup>eds</sup>  
to a normal power up/start up routine at 426.